

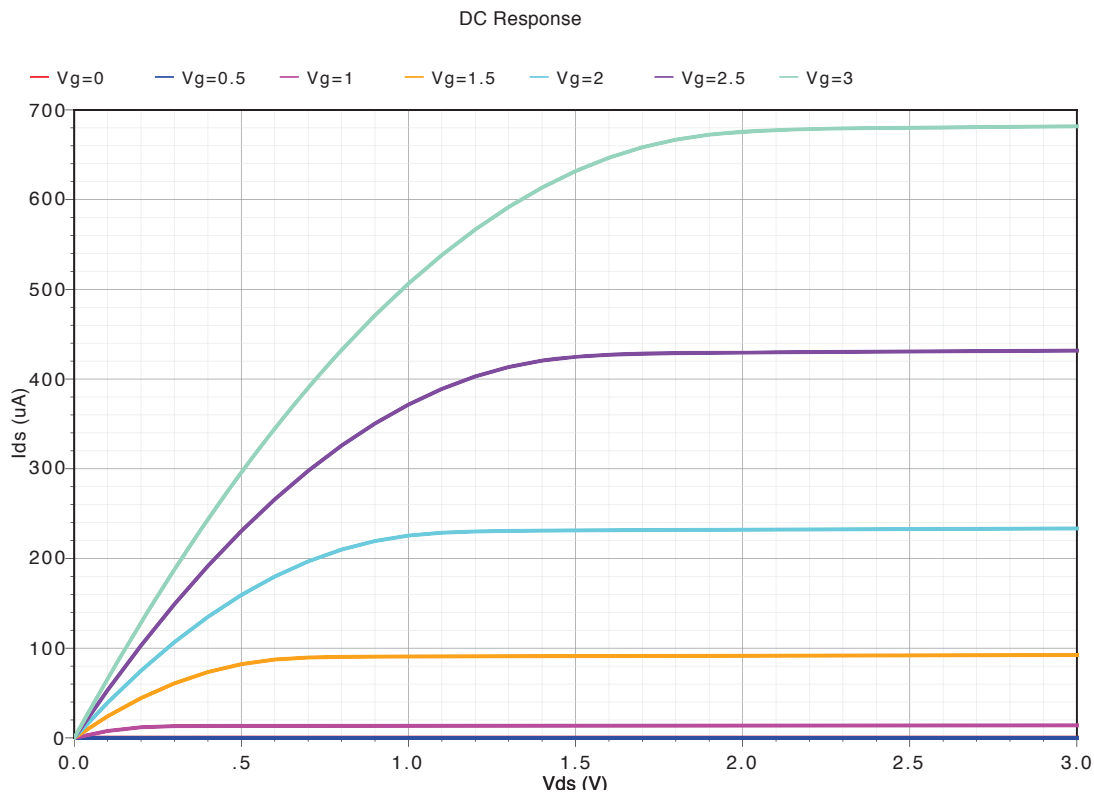


Figure 7: V_{ds} vs I_{ds} for different gate voltages.