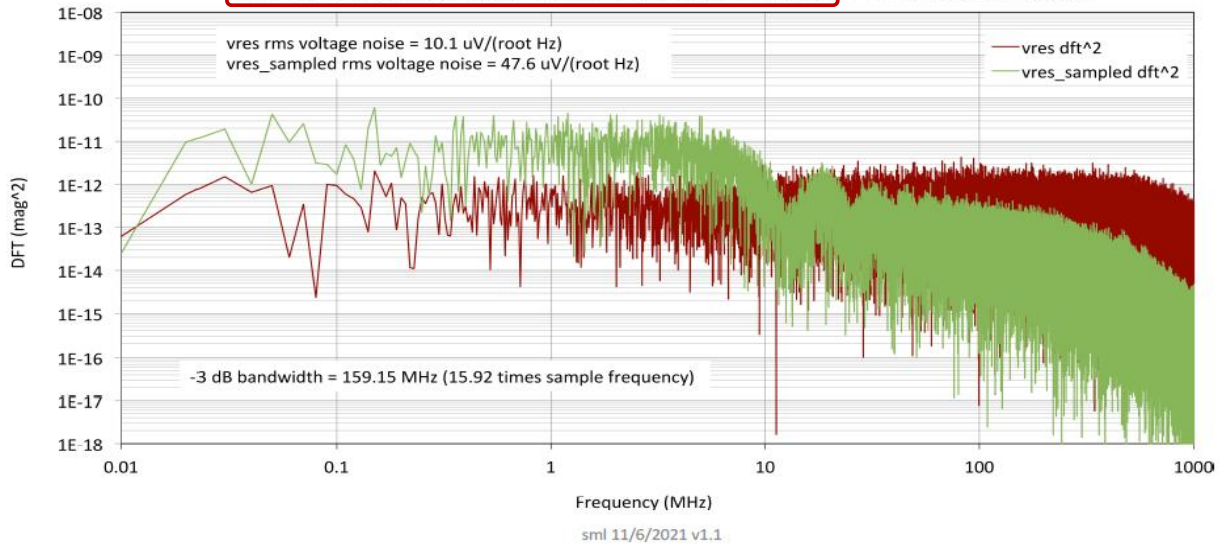


### load\_pF = 1, Single Switch Active (25% duty cycle, fs\_MHz = 10)

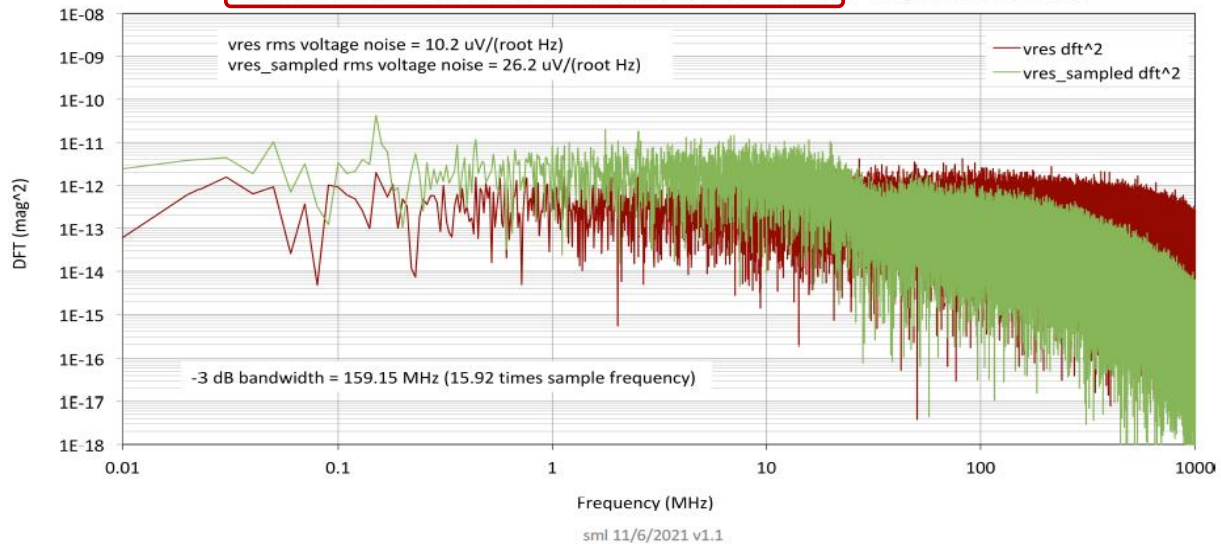
DFT of Noisy Resistor (nodes vres and vres\_sampled) versus Frequency  
res\_ohms = 1000 ohms, load\_pF = 1.0, 125°C, fs = 10.0 MHz, Duty cycle = 25%, voltage noise integration between [0.100, 5.0] MHz  
Number of switches: 1 with 25% non-overlapping duty cycle control signals, N = 262,144 over 1000 periods



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### load\_pF = 1, Two Parallel Switches Active (25% duty cycle, non-overlapping, fs\_MHz = 10)

DFT of Noisy Resistor (nodes vres and vres\_sampled) versus Frequency  
res\_ohms = 1000 ohms, load\_pF = 1.0, 125°C, fs = 10.0 MHz, Duty cycle = 25%, voltage noise integration between [0.100, 5.0] MHz  
Number of switches: 2 with 25% non-overlapping duty cycle control signals, N = 262,144 over 1000 periods



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