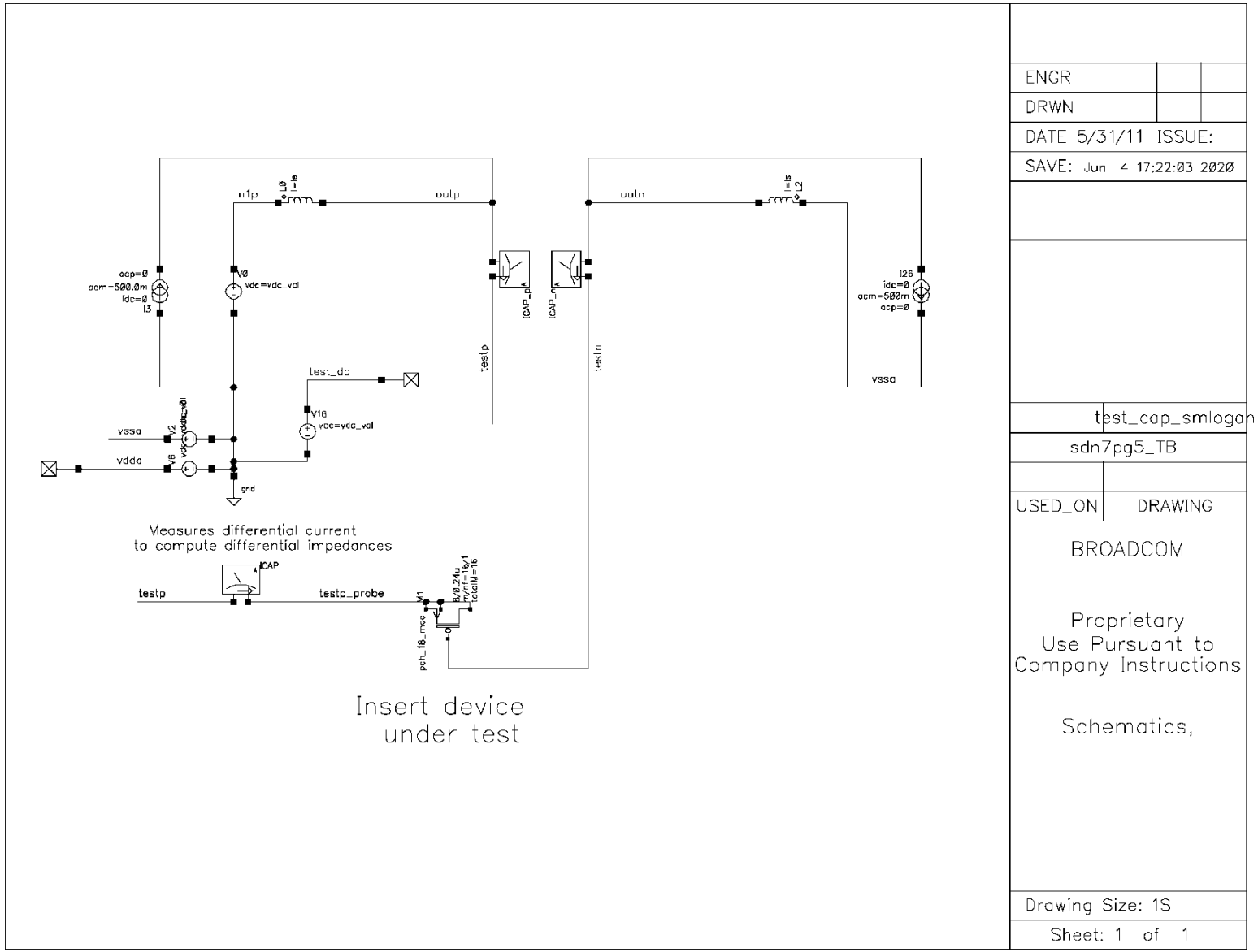
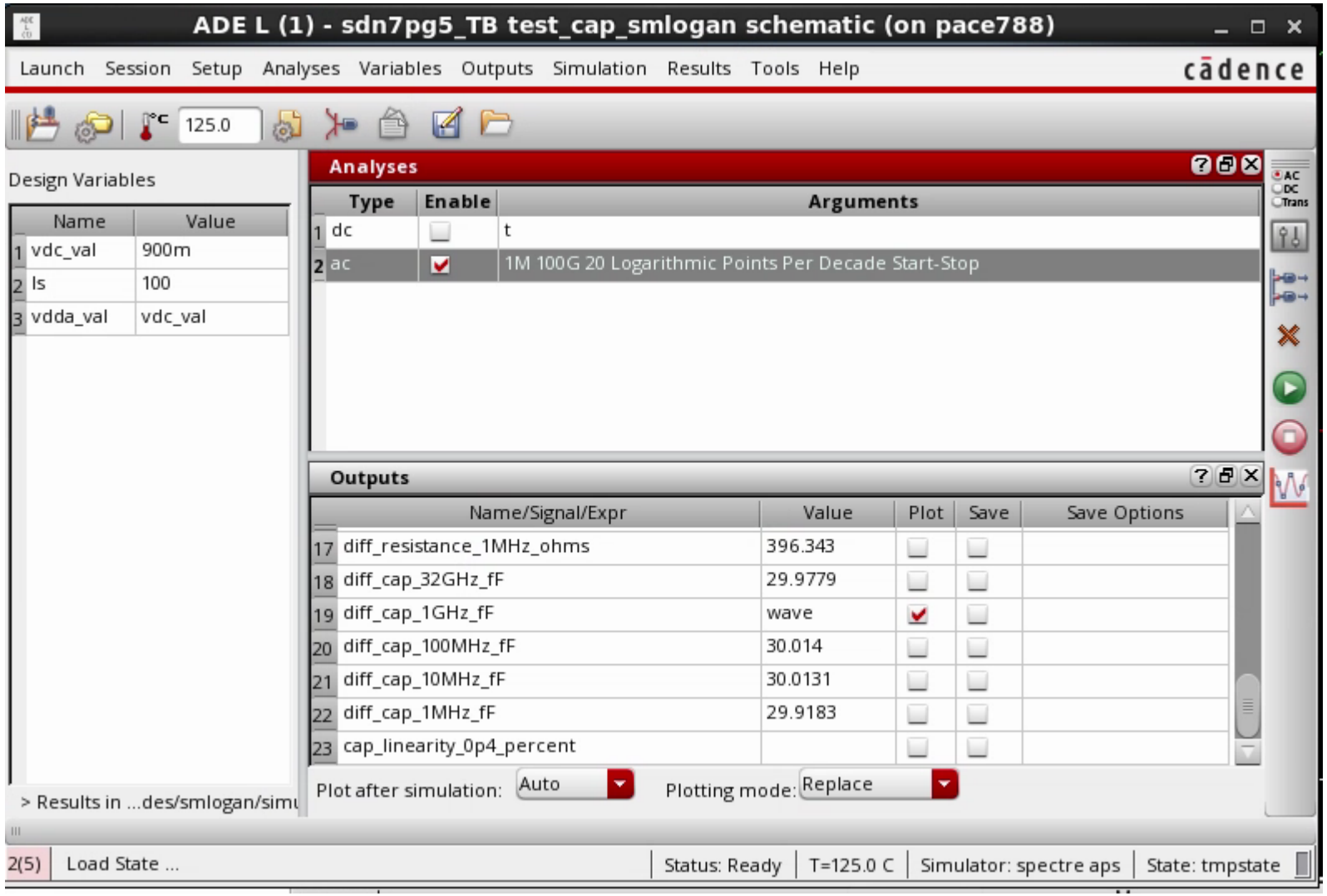
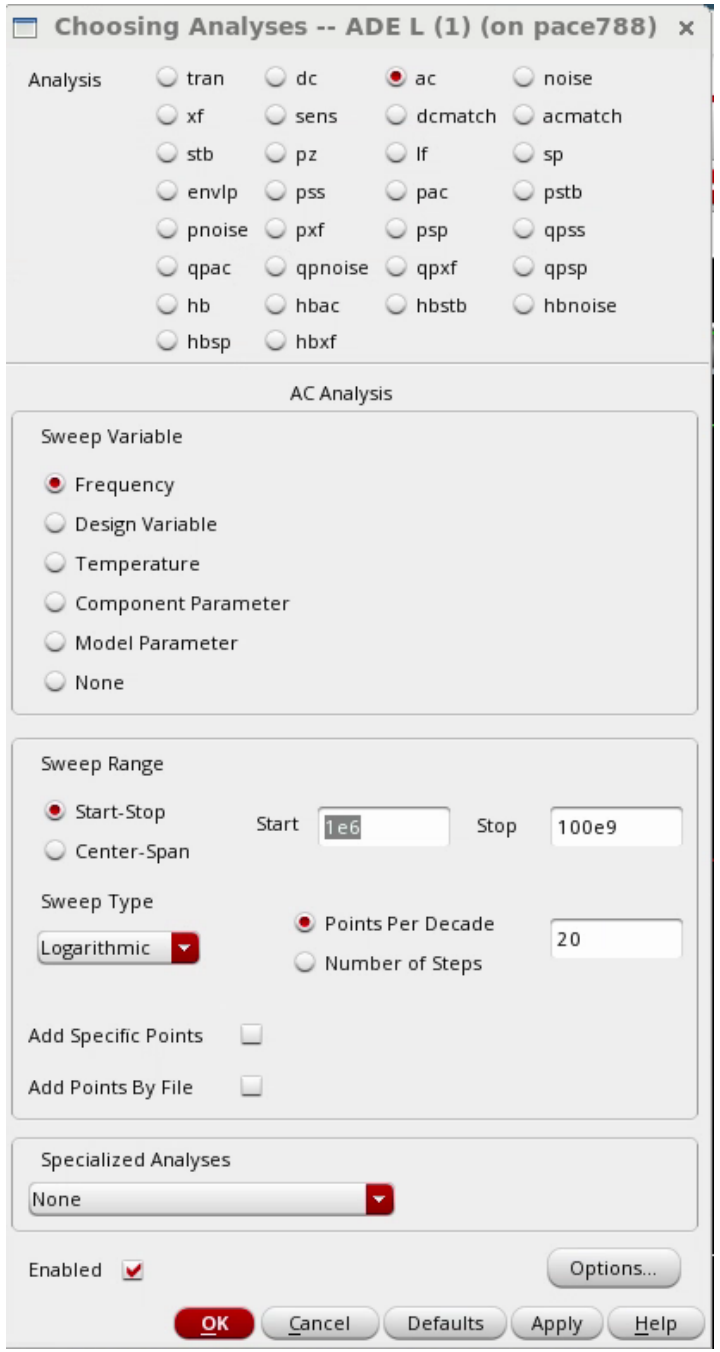
Example Virtuoso Schematic “test_cap_smlogan” v1.2 to Determine Small-signal Impedances at One or More Frequencies



Example ADE-L GUI and Simulation Selection for Impedance Computations





Example ADE-L Graphical Output for Impedance Computations at 1 GHz as a Function of Bias Voltage from Parametric or ADE-XL/Assembler Simulations (uses expressions for capacitance and real components in ADE-L output file)