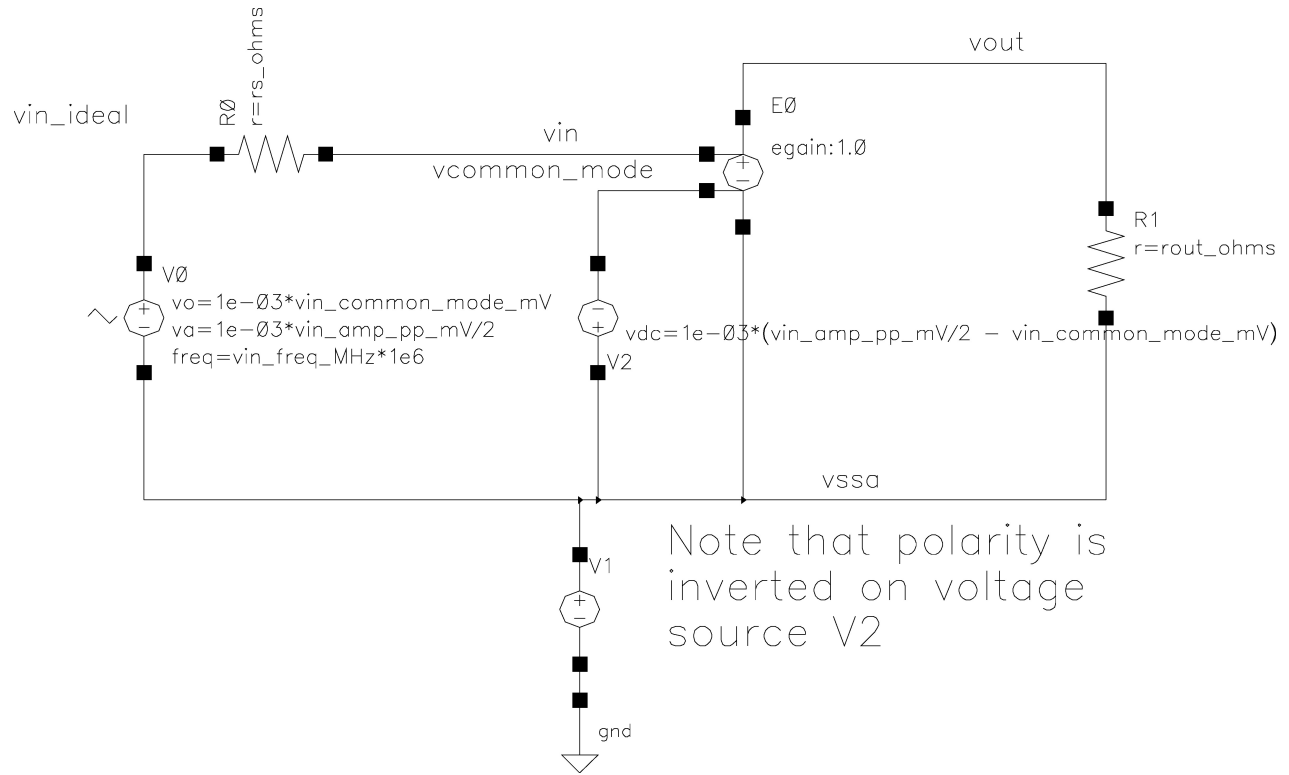
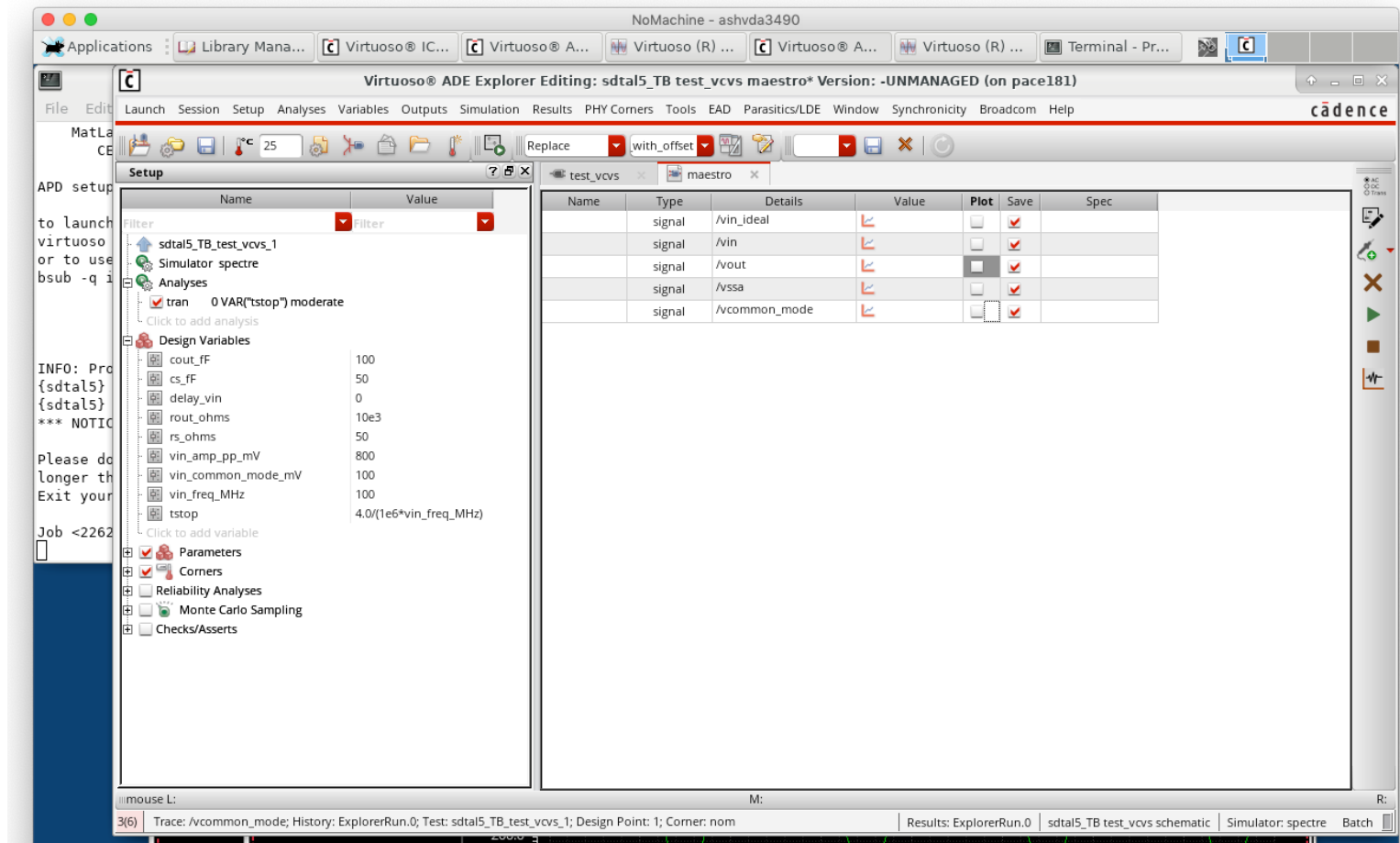


Virtuoso
 detailing using
 an analogLib
 vcvs to level
 shift sinusoid
 with non-zero
 common mode



Note that polarity is inverted on voltage source V2

ADE Explorer Illustrating Analysis and Variables



Example of Using VCVS with Offset Voltage to Level Shift all Values Greater than Zero V

Name
/vin
/vout
/vcommon_mode

ViVA Illustrating Input and Output Signals and DC voltage negative input terminal of vcv

