Assura (tm) Physical Verification Version av4.1:Production:dfII6.1.6-64b:IC6.1.6-64b.9

 Release 4.1\_USR3

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@(#)$CDS: assura\_64 version av4.1:Production:dfII6.1.6-64b:IC6.1.6-64b.9 07/17/2011 14:08 (sjfql593) $

sub-version 4.1\_USR3, integ signature 2011-07-17-1308

run on vlsi-9.nitr.in from /cad/cadence/ASSURA/lnx86/tools.lnx86/assura/bin/64bit/assura on Fri Jun 11 17:04:33 2021

Loading tech rule set file : /home/guests/phd/pjayanta/UMC\_90/RuleDecks/Assura/LVS/techRuleSets

Checking out 1 license for Assura\_RCX 3.10

Checking out 1 license for Assura\_RCX-FS 3.10

Starting /cad/cadence/ASSURA/lnx86/tools/assura/bin/rcxToDfII /home/guests/phd/pjayanta/UMC\_90/rcx.Inverter1.rsf -t -cdslib /home/guests/phd/pjayanta/UMC\_90/cds.lib -libdefs /home/guests/phd/pjayanta/UMC\_90/lib.defs

Virtuoso Framework License (111) was checked out successfully. Total checkout time was 0.06s.

\*WARNING\* lib.defs files and the -libdefs option are no longer supported. The file '/home/guests/phd/pjayanta/UMC\_90/lib.defs' will be ignored. The cds.lib file '/home/guests/phd/pjayanta/UMC\_90/cds.lib' takes precedence.

@(#)$CDS: rcxToDfII\_64 version av4.1:Production:dfII6.1.6-64b:IC6.1.6-64b.9 07/17/2011 14:11 (sjfql593) $

sub-version 4.1\_USR3, integ signature 2011-07-17-1308

run on vlsi-9.nitr.in from /cad/cadence/ASSURA/lnx86/tools.lnx86/assura/bin/64bit/rcxToDfII on Fri Jun 11 17:04:34 2021

Loading tech rule set file : /home/guests/phd/pjayanta/UMC\_90/RuleDecks/Assura/LVS/techRuleSets

Loading umc90nm/libInit.il ...

 Loading umc90nm/loadCxt.ile ... done!

 Loading context 'forInd' from library 'umc90nm' ... done!

 Loading context 'Util' from library 'umc90nm' ... done!

 Loading context 'umc90nm' from library 'umc90nm' ... done!

 Loading context 'pdkUtils' from library 'umc90nm' ... done!

 Loading context 'RF\_CB\_all' from library 'umc90nm' ... done!

 Loading context 'pcellUtil' from library 'umc90nm' ... done!

 Loading context 'MM\_other\_CB' from library 'umc90nm' ... done!

 Loading context 'oxf\_cb' from library 'umc90nm' ... done!

 Loading umc90nm/.cdsenv ... done!

 Loading umc90nm/libInitCustomExit.il ... done!

Loaded umc90nm/libInit.il successfully!

\*WARNING\* No library model for device "NPN\_V32X32".

\*WARNING\* No library model for device "N\_PG126\_SPHVT".

\*WARNING\* No library model for device "N\_PD126\_SPHVT".

\*WARNING\* No library model for device "P\_L126\_SPHVT".

\*WARNING\* No library model for device "N\_PG99\_SPHVT".

\*WARNING\* No library model for device "N\_PD99\_SPHVT".

\*WARNING\* No library model for device "P\_L99\_SPHVT".

\*WARNING\* No library model for device "N\_PG99\_LLHVT".

\*WARNING\* No library model for device "N\_PD99\_LLHVT".

\*WARNING\* No library model for device "P\_L99\_LLHVT".

\*WARNING\* No library model for device "N\_PG116\_SPHVT".

\*WARNING\* No library model for device "N\_PD116\_SPHVT".

\*WARNING\* No library model for device "P\_L116\_SPHVT".

\*WARNING\* No library model for device "N\_PG116\_LLHVT".

\*WARNING\* No library model for device "N\_PD116\_LLHVT".

\*WARNING\* No library model for device "P\_L116\_LLHVT".

\*WARNING\* No library model for device "N\_PG126\_LL\_HVT".

\*WARNING\* No library model for device "N\_PD126\_LL\_HVT".

\*WARNING\* No library model for device "P\_L126\_LL\_HVT".

\*WARNING\* No library model for device "N\_PG240\_SPHVT".

\*WARNING\* No library model for device "N\_PD240\_SPHVT".

\*WARNING\* No library model for device "P\_L240\_SPHVT".

\*WARNING\* No library model for device "N\_PG208\_SPHVT".

\*WARNING\* No library model for device "N\_PD208\_SPHVT".

\*WARNING\* No library model for device "P\_L208\_SPHVT".

\*WARNING\* No library model for device "N\_PG208\_LLHVT".

\*WARNING\* No library model for device "N\_PD208\_LLHVT".

\*WARNING\* No library model for device "P\_L208\_LLHVT".

\*WARNING\* No library model for device "N\_PG199P4\_SPHVT".

\*WARNING\* No library model for device "N\_PD199P4\_SPHVT".

\*WARNING\* No library model for device "P\_L199P4\_SPHVT".

\*WARNING\* No library model for device "N\_PG199P4\_LLHVT".

\*WARNING\* No library model for device "N\_PD199P4\_LLHVT".

\*WARNING\* No library model for device "P\_L199P4\_LLHVT".

\*WARNING\* No library model for device "L\_CR30K\_RFVIL ivpcell".

\*WARNING\* No library model for device "L\_SY30KCT\_RFVIL ivpcell".

\*WARNING\* No library model for device "L\_SY30K\_RFVIL ivpcell".

\*WARNING\* No library model for device "L\_SQSK\_RFVIL ivpcell".

Finished /cad/cadence/ASSURA/lnx86/tools/assura/bin/rcxToDfII

Starting /cad/cadence/ASSURA/lnx86/tools/assura/bin/avRCXxref /home/guests/phd/pjayanta/UMC\_90/rcx.Inverter1.rsf

@(#)$CDS: avRCXxref\_64 version av4.1:Production:dfII6.1.6-64b:IC6.1.6-64b.9 07/17/2011 14:08 (sjfql593) $

sub-version 4.1\_USR3, integ signature 2011-07-17-1308

run on vlsi-9.nitr.in at Fri Jun 11 17:04:35 2021

Reading rsf

Finished /cad/cadence/ASSURA/lnx86/tools/assura/bin/avRCXxref

Constructing the RCX run script

\*ERROR\* at "RCXspice": Unable to read '/home/guests/phd/pjayanta/UMC\_90/RuleDecks/Assura/LVS/../LPE/Option10/rcxfs.dat', No such file or directory

\*WARNING\* Bad return status from RCX script generator. 0x100