Notes on IBIS model for HFBR 5912

The IBIS model for HFBR 5912 consists of 4 files, hfbr5912.ibs, r100.ibs, c01.ibs, and 5912e.ebd. File hfbr5912.ibs is an IBIS file based only on spice simulations and describes transceiver behavior in the typical, minimum, and maximum ranges of operation. Model verification has taken place with Spice simulations as well as measurements.

C01.ibs represents the coupling caps, r100.ibs is the 100 ohm terminating resistor, and 5912e.ebd is the electrical board description of parasitics from the pin of the transceiver to the pin of the die.

If the ebd file is not used, please note there is a 100Ω terminating resistor between pins 9 and 10, as well as AC coupling capacitors along the signal lines of pins 9 and 10.

Please see data sheet if further clarification is needed.

Software Compatibility

Certain software will not be able to run ebd files that describe 2 pins terminated with a resistor between them. The .ibs file, however, should work with all major software.

Customers using XTK must convert the IBIS model into an XTK model. Since this IBIS model includes min, typ, and max cases in one file, users of XTK will need to separate the IBIS model into 3 different files and make an internal name change within the files.