

## **Creation of footprint for Analog Devices**

"This footprint is a 'suggested' library component. It is derived from the package dimensions provided in the device datasheet. The body and pin positions are in accordance to the dimensions provided in the datasheet. The footprint padstack is created in accordance to IPC-SM-782A guidelines. The supplied padstack may be replaced with another padstack. Footprint properties may be modified, added or removed. The layers provided may have to be altered to match specific design rules and CAD setup. The user is advised to independently analyze the footprint to ensure that it meets the PCB fabrication process & assembly process requirements of the designated manufacturing facility."

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