Action 34. Click Setup — Connect Rules. In the Select Connect Rules form,

highlight ConnRules\_5V\_full in the List of Connect Rules Used in

Simulation and click Delete to remove the default selection. In the

Built-in rules section, click connectLib. ConnRules\_3V\_full\_fast in the

Rules Name cyclic field.

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Because the power supply in this PLL is 2.5 V, which doesn’t exist in the Built-in

rules list, you need to customize the rule.

Action 35. Click Customize. In the Customize Built-in Rules form, change the

Description to “This is the description for

My\_ConnRules\_25V\_full\_fast.”

In the Connect Module Declarations list, click on L2E\_2. In the

Parameters list, click vsup, change 3.3 to 2.5 in Value field and click

Change.

Next highlight both E2L\_2 and Bidir\_2 and change the values of the

following parameters:

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E2L\_2 vsup=2.5 vthi=1.7 vtlo=0.8

Bidir\_2 vsup=2.5 vthi=1.7 vtlo=0.8

Click OK. In the Information form, which reminds you to add the customized

connect rules to the list, click OK.

Action 36. In the Select Connect Rules form, click Add and select the new

modified connect rules. Click Rename and edit the rule name to

My\_ConnRules\_25V\_full\_fast. Click OK.

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Action 37. In the ADE window, click Outputs — To Be Plotted — Select on

Schematic and click CLK\_REF and CLK\_160MHZ on the top level.

Then repeat for vCNTL and VCO\_CLK in the I3 instance.